

Appln. No.: 10/743,385

Preliminary Amendment dated March 17, 2004



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Re the Application of:

**Shigeru ATSUMI et al.**

Serial No.: 10/743,385

Filed: December 23, 2003

For: SEMICONDUCTOR INTEGRATED  
CIRCUIT DEVICE WITH ERASABLE AND  
PROGRAMMABLE FUSE MEMORY (as  
amended)

Atty. Docket No.: 001701.00197

Group Art Unit: Unknown

Examiner: Unknown

Confirmation No.: Unknown

**PRELIMINARY AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Prior to examination, please amend the instant application as follows:

**Amendments to the Specification** begin on page 2 of this paper.

**Amendments to the Claims** are reflected in the Listing of Claims, which begins on page 3 of this paper.

**Remarks/Arguments** begin on page 5 of this paper.

An **Appendix** including an Abstract of the Disclosure is attached following page 5 of this paper.